
RAM & ROM Based Digital Design

ECE 152A – Winter 2012

Reading Assignment

- Brown and Vranesic
 - 10 Digital System Design
 - 10.1 Building Block Circuits
 - 10.1.3 Static Random Access Memory (SRAM)
 - 10.1.4 SRAM Blocks in PLDs

Reading Assignment

- Roth
 - 9 Multiplexers, Decoders, and Programmable Logic Devices
 - 9.5 Read Only Memories

End of ebook preview

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